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UTILITY
PATENT APPLICATION
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to respond to a concernor	2
Attorney Docket No.	1880-031249
First Inventor	Robert J. Hillard
Title 'METHOD OF	ELECTRICAL CHARACTERIZATION OF A"
Everona Mail Label N	C EL 071291925 US

PATEN	NT APPLICATION	First Ir	iventor Robert J. Hillar	·a		
TF			'METHOD OF ELECTRICAL CHARACTERIZATION OF A"			
	onal applications under 37 C.F.R. 1.53(s Mail Label No. EL 9712818	25 US		_	
	PLICATION ELEMENTS 600 concerning utility patent application	contents	ADDRESS TO:	Mail Sto P.O. Box	sioner For Patents op Patent Application x 1450 ria VA 22313-1450	
	nittal Form (e.g., PTO/SB/17) n original and a duplicate for fee pro	ocessing)	7. CD-ROM or CD-Computer Program	•	icate, large table or dix)	0
2. x Applicant of See 37 Cl	claims small entity status. FR 1.27.		8. Nucleotide and/or Amino (if applicable, all necessa	Acid Sec ary)	quence Submission	3. PT 341
3. x Specification (preferred	on [Total Pages arrangement set forth below)	12]	a. Computer Read	dable Fori	m (CRF)	7018
•	ve title of the invention ference to Related Applications	•	b. Specification S			10/1
- Statemen	at Regarding Fed sponsored R & D e to sequence listing, a table,	ı	i. CD-ROM	1 or CD-R	t (2 copies); or	
or a com	puter program listing appendix and of the Invention			ifving ide	ntity of above copies	
	nmary of the Invention					
	scription of the Drawings (if filed)		ACCOMPANYIN	NG APPL	LICATION PARTS	
- Detailed - Claim(s)	Description		9. x Assignment Paper	rs (cover :	sheet & document(s))	. ,
. '	of the Disclosure		10. 37 CFR 3.73(b) S (when there is an		Power Attorn	
4. x Drawing(s)) (35 U.S.C. 113) [Total Sheets	6]			ment (if applicable)	
5. Oath or Declaration [Total Pages 2] Information Disclosure Statement (IDS)/PTO-1449 Copies of ID Citations						
a. x N	ewly executed (original or copy)		13. Preliminary Amen		4DCD 502)	
	opy from a prior application (37 CFR or continuation/divisional with Box 18 con	npleted)	(Should be specifi	cally iten	nized)	,
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inventor(s) named in the prior application, see 37 CFR 1.63(d)(2) and 1.33(b). Nonpublication Request under 35 U.S.C. 122 (b)(2)(B)(i). Applicant must attach form PTO/SB/35 or its equivalent.				3/35 or		
6. Application	n Data Sheet. See 37 CFR 1.76			for \$385	.00 and \$40.00	
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following the title, or it	an Application Data Sheet under 37 CFI	R 1.76:	ppy the requisite information core is			
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For CONTINUATION	N or DIVISIONAL APPS only: The ensidered a part of the disclosure	ntire disclos	ure of the prior application, from	n which a	n oath or declaration n and is hereby incorr	is supplied
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Name (Print/Type)	William/A. Logsdon	P	Registration No. (Attorney/A	gent)	22,132	
Signature	10/ us 14.	y	4	Date	November 5, 2003	
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This collection of information is required by 37 CFR 1.53(b). The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality if governed by 35 J. S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you are required to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF:	ATTORNEY'S DOCKET NUMBER
ROBERT J. HILLARD	1880-031249
	CAL CHARACTERIZATION OF A SILICON-ON- SULATOR (SOI) WAFER"
Commissioner for Patents Mail Stop Patent Application P.O. Box 1450 Alexandria, VA 22313-1450	*
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PATENT APPLICATION TRANSMITTAL LETTER

Commissioner for Patents Mail Stop Patent Application P.O. Box 1450 Alexandria, VA 22313-1450

Transmitted herewith for filing is the patent application of: ROBERT J. HILLARD

Entitled: "METHOD OF ELECTRICAL CHARACTERIZATION OF A SILICON-ON-**INSULATOR (SOI) WAFER"**

Small Entity Status:					
	all Entity Statement un all Entity Status is asso			7 CFR 1.2	27
and	pages of Specification 6 pages of drawings Assignment of the inv		•		ge Abstract)
x Dec	laration				
Non	-Publication Request	Under 35 U.S.C	. §122(b)(2)(B)	(i)	
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	CLAIN	MS AS FILED			40
No. Filed <u>N</u> o	o. Extra	Small Entity Rate	Non-Small Entity Rate		Charge
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Claims 15 -20 =	0	x \$ 9.00	x \$ 18.00		\$ 0
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Claims $2 -3 = $	0	x \$ 43.00	x \$ 86.00 ,		\$ 0
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Basic Fee		+ \$385.00	+ \$770.00	-	\$385.00
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November 5, 2003	By Way It. Toda
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